

STATEMENT OF THE CLAIMS

1. (currently amended) A video signal processor comprising:

an input that receives an input video signal of a first definition and comprising two interlaced fields;

resizing circuitry, operably coupled to said input, that processes said input video signal to provide a picture component signal corresponding thereto;

analysis circuitry, operably coupled to said input, that processes said input video signal to provide a measurement component signal representing a graphical representation of at least one characteristic associated with said input video signal;

a video signal generator that generates a background video signal of a second higher definition and comprising two interlaced fields; and

combining circuitry, operably coupled to said resizing circuitry, said analysis circuitry and said video signal generator, that superimposes said picture component signal and said measurement component signal onto said background video signal to provide an output video signal of the second higher definition and comprising two interlaced fields.

2. (cancelled)

3. (currently amended) A video signal processor as claimed in claim 1, wherein:

the first definition comprises a first plurality of horizontal lines per frame and the second definition comprises a second plurality of horizontal lines per frame higher than ~~different from~~ the first plurality of horizontal lines per frame.

4. (cancelled)

5. (currently amended) A video signal processor as claimed ~~claim~~ in claim 3 [[4]],
wherein:

the first plurality of horizontal lines per frame comprises 625 horizontal lines per frame and said second plurality of lines per frames comprises 1125 horizontal lines per frame.

6. (currently amended) A video signal processor as claimed in claim 1 2, wherein:

said output video signal represents a high definition picture suitable for display on a high definition display screen.

7. (original) A video signal processor as claimed in claim 6, wherein:

said picture component signal represents a reduced-size picture suitable for display on part of the high definition display screen.

8. (original) A video signal processor as claimed in claim 1, wherein:

said output video signal provides for display of a reduced-size picture represented by the picture component signal in one quarter of a display screen, a video waveform in a second quarter of the display screen, a vector diagram in a third quarter of the display screen, and audio information in a further quarter of the display screen, and

wherein said video waveform, said vector diagram and said audio information represent characteristics of said input video signal.

9. (currently amended) A video signal processor which comprises an input for receiving a video signal of a first definition comprising two interlaced fields, resizing means for processing said input video signal to provide a picture component signal for creating a picture display of reduced size, analyzing means for processing said input video signal to provide a measurement component signal for creating a display of a graphical representation of at least one characteristic associated with said input video signal, a video signal generator for generating a video signal of a second higher definition comprising two interlaced fields for providing a background display, and means for combining said picture component signal, said measurement component signal and said video signal of the second higher definition, to provide an output video signal of the second higher definition and comprising two interlaced fields.

10. (original) A video signal processor as claimed in claim 9 arranged to receive a said video input of a relatively low definition and said video signal generator is arranged to generate a video signal of relatively high definition.

11. (original) A video signal processor as claimed in claim 9, in which said output video signal serves for the display of the picture in one part of a display screen and a said graphical representation in another part of said screen.

12. (original) A video signal processor as claimed in claim 9, in which said output video signal serves for the display of the picture in one quarter of a display screen, a video waveform in a second quarter of said screen, a vector diagram in a third quarter of said

screen, and audio information in a fourth quarter of said screen, said video waveform, vector diagram and audio information being respective said characteristics associated with said input video signal.

13. (currently amended) A video signal processor which comprises an input for receiving a video signal arranged for the display on a screen of a picture comprising a first plurality of horizontal lines per frame made up of two interlaced fields, resizing means for processing said input video signal to provide a picture component signal for the display on said screen of a said picture of reduced size, analyzing means for processing said input video signal to provide a measurement component signal for the display of a graphical representation of at least one characteristic associated with said input video signal, a video signal generator for generating a video signal for providing a background display and comprising a second plurality of horizontal lines per frame made up of two interlaced fields, said second plurality of horizontal lines being greater in number than said first plurality of horizontal lines, and means for combining said picture component signal, said measurement component signal and said video signal generated by said video signal generator, to provide an output video signal comprising two interlaced fields.